



8-12-04

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Timothy D. Van Tassel

Application No: 10/757,833

Filing Date: January 14, 2004

Title: Electronic Circuit with Spring
Reverberation Effect and Improved
Output Controllability

Group Art Unit: 2644

Date: August 11, 2004

INFORMATION DISCLOSURE STATEMENT

I hereby certify that this correspondence and supportive enclosures (as listed on last page) are being deposited with the United States Postal Service with sufficient postage as Express Mail (#ER644119186US) in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on August 11, 2004.

Michael A. Mochinski
PTO Reg. No. 42,900

Date of Signature

Honorable Commissioner of Patents and Trademarks

Alexandria, VA 22313

Sir:

Applicant submits this Statement in accordance with the duty of disclosure under 37 C.F.R. § 1.56 and §§ 1.97-1.98. This Statement is filed in accordance with 37 C.F.R. § 1.97(b)(3), prior to issuance of a First Office Action on the merits.

Disclosed as the first non-patent publication is a technical article entitled "Using Op Amps to Interface to Spring-type Reverb Units," which describes therein means for optimally

interfacing a spring reverb unit to an op amp, including further description of the drive circuit, optimal drive level, and signal recovery circuit.

Disclosed as the second non-patent publication is an excerpt from the book entitled "Electronic Projects for Musicians," which describes therein a preamp circuit (see schematic, p. 58) for boosting the signal level of any instrument, such as a guitar, plugged into its input at J1, with the resultant boosted signal exiting at the circuit's output at J2, primarily at a predetermined gain suitably established by the circuit's internal resistance (either by way of fixed or variable resistive means).

Disclosed as the third non-patent publication is an excerpt from the book entitled "Do-it-yourself Projects for Guitarists," which describes therein a preamp circuit comprising a dual low-noise op amp, with the first op amp (IC1a) having the capacity to convert the instrument's output from high-impedance to low-impedance, while adding approximately 20 dB gain, and the second op amp (IC1b) having the capacity for additional output for balanced line operation (see Fig. 4-11, p. 50).

Disclosed as the fourth non-patent publication are assembly instructions for the 20626 (JE215) dual power supply manufactured by Jameco Electronic Components/Computer Products, which comprises circuitry means for adjusting positive and negative output voltages, notably useful in meeting IC circuit voltage requirements.

A copy of each reference, as itemized on forms PTO/SB/08A and PTO/SB/08B, is submitted herewith.

Applicant respectfully solicits the Examiner's consideration of the cited references and entry thereof in the record of this application.

Dated this 11th day of August 2004.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Michael A. Mochinski", written over a horizontal line.

Michael A. Mochinski
USPTO Reg. No. 42,900
3300 Bass Lake Road, Suite 514
Brooklyn Center, Minnesota 55429
Telephone: (763) 585-0283



- Enclosures: 1) Information Disclosure Statement by Applicant (forms PTO/SB/08A and PTO/SB/08B)
- 2) Complete Copy of Patent 4,489,439
- 3) Excerpt from Book Entitled "Do-it-yourself Projects for Guitarists"
- 4) Excerpt from Book Entitled "Electronic Projects for Musicians"
- 5) Technical Article Entitled "Using Op Amps to Interface to Spring-type Reverb Units"
- 6) 20626 Kit Assembly Instructions
- 7) Return Receipt Postcard



(Use as many sheets as necessary)

Sheet	1	of	2
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Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Complete if Known

Application Number	10/757,833
Filing Date	01/14/2004
First Named Inventor	Timothy D. Van Tassel
Art Unit	2644
Examiner Name	
Attorney Docket Number	03-1201-US

U. S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

**Examiner
Signature**

Date
Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Substitute for form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Sheet	2	of	2	Application Number	10/757,833
				Filing Date	01/14/2004
				First Named Inventor	Timothy D. Van Tassel
				Art Unit	2644
				Examiner Name	
				Attorney Docket Number	03-1201-US

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		ROY MALLORY, "Using Op Amps to Interface to Spring-type Reverb Units," Online Tech Article Appearing on Website (www.members.tripod.com/~roymal/reverb.htm), November 2001	
		CRAIG ANDERTON, Project No. 1: Preamp, "Electronic Projects for Musicians," 1975, pp. 56-60, Guitar Player Books, New York, NY	
		CRAIG ANDERTON, Project No. 9: Beat the DI Blues with Iggy, "Do-it-yourself Projects for Guitarists," 1995, pp. 48-51, Miller Freeman Books, San Francisco, CA	
		Manufacturer Instruction Literature, Jameco Electronic Components/Computer Products, 20626 Kit Assembly Instructions, Belmont, CA	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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